

JC997 U.S. PTO
09/09/2013



ISSUE CLASSIFICATION	
Class	Subclass

BEST AVAILABLE COPY

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.  PATENT DATE

18/01/2010 SCANNED 500 Q.A. 

APPLICATION NO. 09/882538	CONT/PRIOR	CLASS 257	SUBCLASS 197	ART UNIT 2811 2814	EXAMINER EALAHALL
------------------------------	------------	--------------	-----------------	--------------------------	----------------------

Alexander Kalnitsky
Michael Rowlandson
Ken Liao
Robert Scheer

Method for forming a super self-aligned hetero-junction bipolar transistor

TITLE transistor

PTO-2040
12/99

ISSUING CLASSIFICATION

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				Amount Due	Date Paid
				ISSUE BATCH NUMBER	
WARNING: This form is for use in connection with the filing of a terminal disclaimer. It is not to be used in connection with the filing of a continuation or a divisional application. If you are filing a continuation or a divisional application, you must use the appropriate form for that purpose.					

WARNING:

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-436A
(Rev. 6/99)

FILED WITH: DISK (CRF) FICHE CD-ROM
(Attached in pocket on right inside flap)